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"In the FM transmitter of the invention, all the components except for the quartz oscillator Xosc and the modulation elements, are integrated in a single semiconductor chip. The chip may be formed by BiCMOS processes. Analog signal processor sections 50 and 60, stereo modulation section 70, FM circuit 90, and RF amplifiers 102 and 103 may be provided in the form of bipolar circuits. PLL frequency synthesizer 80, which is a digital or pulse signal processor, and shift register 101 may be CMOS circuits."

Please replace the section of the specification entitled ABSTRACT OF THE DISCLOSURE on page 13, lines 1-17, with the following text:

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"ABSTRACT OF THE DISCLOSURE

A frequency modulating (FM) transmitter includes a reference frequency generator, a reference frequency divider, a stereo modulation circuit, an oscillator circuit, a program counter, and a PLL frequency synthesizer. The reference frequency generator is for generating a reference frequency. The reference frequency divider is for frequency dividing the reference frequency. The stereo modulation circuit is for frequency modulating audio signals by using one output of the reference frequency divider to supply resultant stereo modulated signals as FM signals. The oscillator circuit is for generating carrier waves to transmit the FM signals. The program counter is for frequency dividing the carrier waves into variable frequency components. The PLL frequency synthesizer has a phase comparator circuit for comparing the variable frequency components output from the program counter with another output of the reference frequency divider."

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IN THE CLAIMS:

Please replace the text of claims 1, 2, and 3 with the following text:

1. (Amended) A frequency modulating (FM) transmitter, comprising: